

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

<u>5962-86856</u>	<u>01</u>	<u>R</u>	<u>X</u>
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	54HCT573	Octal D-type latch with three-state outputs and LS TTL compatible inputs.

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

<u>Outline letter</u>	<u>Case outline</u>
R	D-8 (20-lead, 1/4" x 1 1/16"), dual-in-line package

1.3 Absolute maximum ratings. 1/

Supply voltage range	-0.5 V dc to +7.0 V dc
DC input voltage	-0.5 V dc to V_{CC} +0.5 V dc
DC output voltage	-0.5 V dc to V_{CC} +0.5 V dc
Clamp diode current	±20 mA
DC output current (per pin)	±35 mA
DC V_{CC} or GND current (per pin)	±70 mA
Storage temperature range	-65°C to +150°C
Power dissipation (P_D)	500 mW <u>2/</u>
Lead temperature (soldering, 10 seconds)	+260°C
Thermal resistance, junction-to-case (θ_{JC}):	
Case R	(See MIL-M-38510, appendix C)
Junction temperature (T_J)	+175°C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})	+4.5 V dc minimum to +5.5 V dc
Case operating temperature range (T_C)	-55°C to +125°C
Input rise or fall time	0 to 500 ns
Minimum setup time (t_S):	
$V_{CC} = 4.5$ V	
$T_C = +25^\circ\text{C}$	15 ns
$T_C = -55^\circ\text{C}$ to +125°C	22 ns

1/ Unless otherwise specified, all voltages are referenced to ground.

2/ For $T_C = +100^\circ\text{C}$ to +125°C, derate linearly at 12 mW/°C.

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Minimum hold time (t_H):

$V_{CC} = 4.5 \text{ V}$

$T_C = +25^\circ \text{ C} \dots\dots\dots 10 \text{ ns}$

$T_C = -55^\circ \text{ C to } +125^\circ \text{ C} \dots\dots\dots 15 \text{ ns}$

Minimum pulse width (t_W):

$V_{CC} = 4.5 \text{ V}$

$T_C = +25^\circ \text{ C} \dots\dots\dots 20 \text{ ns}$

$T_C = -55^\circ \text{ C to } +125^\circ \text{ C} \dots\dots\dots 25 \text{ ns}$

2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Condition 1/ -55° C ≤ T _C ≤ +125° C 4.5 V dc ≤ V _{CC} ≤ 5.5 V dc (unless otherwise specified)	Grou A subgroups	Limits		Unit
				Min	Max	
High-level ouput voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL} V _{CC} = 4.5 V	1,2,3	4.4		V
		I _O ≤ 20 uA				
		I _O ≤ 6.0 mA		3.7		
Low-level ouput voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL} V _{CC} = 4.5 V			0.1	
		I _O ≤ 20 uA			0.4	
		I _O ≤ 6.0 mA				
High-level input voltage	V _{IH}	V _{CC} = 4.5 V		2.0		uA
Low-level input voltage	V _{IL}	V _{CC} = 4.5 V			0.8	
Input leakage current	I _{IN}	V _{CC} = 5.5 V; V _{IN} = V _{CC} or GND		-1.0	1.0	
Quiescent current	I _{CC}	V _{CC} = 5.5 V; V _{IN} = V _{CC} or GND			160	
Additional quiescent supply current	I _{CC}	V _{IN} = 2.4 V or 0.5 V, any 1 input, V _{IN} = V _{CC} or GND, other inputs V _{CC} = 5.5 V; I _{OUT} = 0 uA			2.9	mA
Three-state output leakage current	I _{OZ}	V _O = V _{CC} or GND; V _I = V _{IH} or V _{IL}		-10	10	uA
Input capacitance	C _{IN}	V _{IN} = 0 V, T _C = +25° C See 4.3.1c	4		10	pF
Output capacitance	C _{IN}	T _C = +25° C, See 4.3.1c	4		20	
Functional tests		See 4.3.1d	7			

See footnotes at end of table.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Condition 1/ -55° C ≤ T _C ≤ +125° C 4.5 V dc ≤ V _{CC} ≤ 5.5 V dc (unless otherwise specified)		Grou A subgroups	Limits		Unit	
					Min	Max		
Propagation delay time, data to Q output (See figure 4)	t _{PHL1} , t _{PLH1}	V _{CC} = 4.5 V C _L = 50 pF ±10%	T _C = +25° C	9		40	ns	
			T _C = -55° C, +125° C	10,11		60		
Propagation delay time, Latch enable to any output (See figure 4)	t _{PHL2} , t _{PLH2}		T _C = +25° C	9		35		
			T _C = -55° C, +125° C	10,11		53		
Propagation delay time, output enable to any output (See figure 4)	t _{PZH} , t _{PZL}		T _C = +25° C	9		35		
			T _C = -55° C, +125° C	10,11		53		
Propagation delay time, output disable to any output (See figure 4)	t _{PHZ} , t _{PLZ}		T _C = +25° C	9		35		
			T _C = -55° C, +125° C	10,11		53		
Output transistion time 2/ (See figure 4)	t _{THL} , t _{TLH}		T _C = +25° C	9		12		
			T _C = -55° C, +125° C	10,11		18		

1/ For a power supply of 5 V ±10% the worst case output voltages (V_{OH} and V_{OL}) occur for HCT at 4.5 V. Thus the 4.5 V values should be used when designing with this supply. Worst case V_{IH} and V_{IL} occur at V_{CC} = 5.5 V respectively.

2/ Transition times (t_{THL}, t_{TLH}), if not tested, shall be guaranteed to the specified parameters.

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CASE R

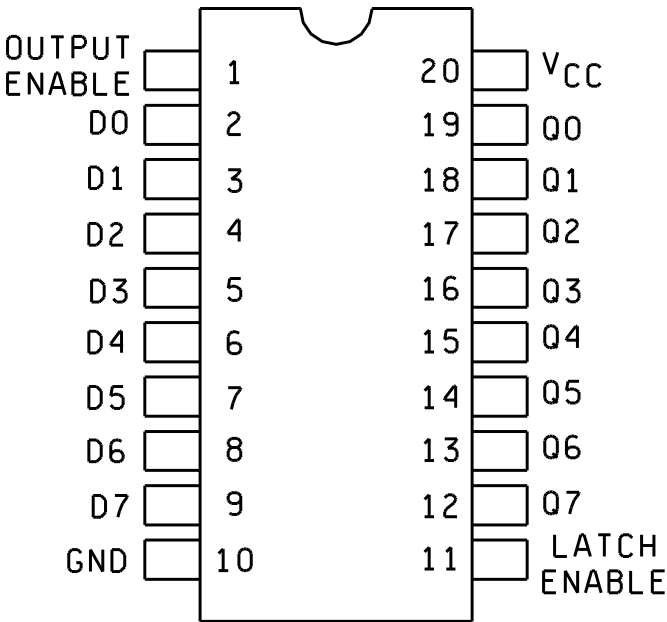


FIGURE 1. Terminal connections (top view).

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Output enable	Latch enable	Data	Output
L	H	H	H
L	H	L	L
L	L	X	Q_o
H	X	X	Z

H = high level, L = low level

Q_o = level of output before steady-state input conditions were established.

Z = high impedance

Figure 2. Truth table.

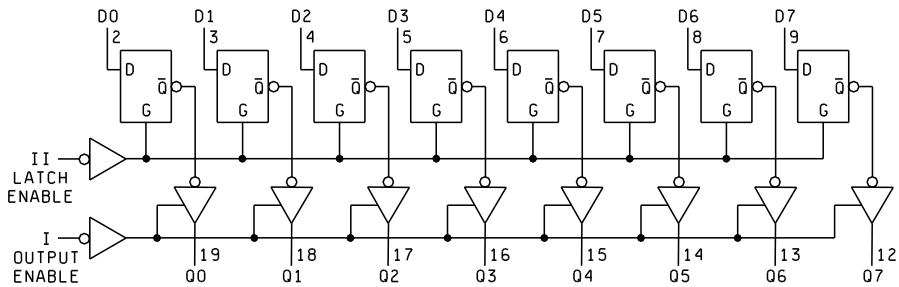


FIGURE 3. Logic diagram.

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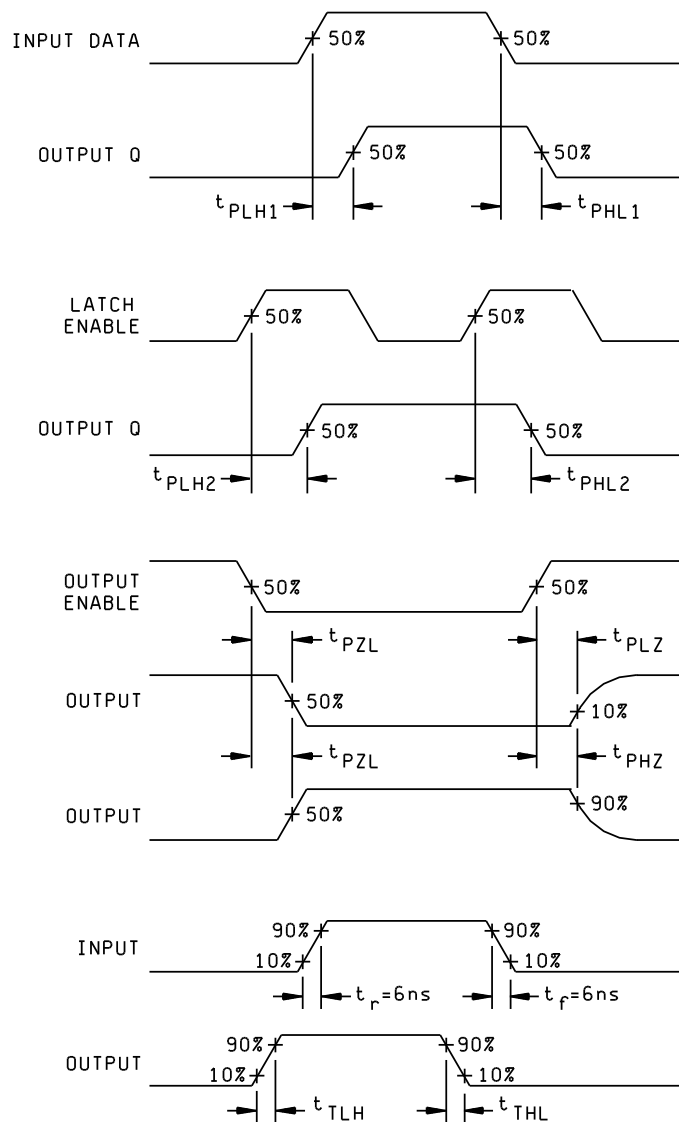


FIGURE 4. Switching waveforms.

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3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (See 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 4 (C_{IN} measurement) shall be measured only for the initial test and after process or design changes which may affect input capacitance.

d. Subgroups 7 tests sufficiently verify the truth table.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*,2,3,9
Group A test requirements (method 5005)	1,2,3,4,7,9,10,11
Group C and D end-point electrical parameters (method 5005)	1,2,3
Additional electrical subgroups for group C periodic inspections	- - -

*PDA applies to subgroup 1.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8685601RX	18714	CD54HCT573F/3A

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE
number

18714

Vendor name
and address

RCA Corporation
Route 202
Somerville, NJ 08876

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